

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

FAST-DISABLED VOLTAGE REGULATOR CIRCUIT WITH
LOW-NOISE FEEDBACK LOOP AND OPERATING METHOD
THEREOF

Application Number : 10/711,618

Confirmation Number:

First Named Applicant: Ling-Wei Ke

Attorney Docket Number: MTKP0080USA

Art Unit: 2838

Examiner: Sterrett

Search string: (6362609 or 6414537 or 5861737 or 5528132 or 4962349 or 5015921).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|------|----------|------------|------------|------------------|------|-------|----------|
| ↑ | 1 | 6362609 | 2002-03-26 | Gailhard | — | 323 | 280 |
| ↑ | 2 | 6414537 | 2002-07-02 | Smith | — | 327 | 540 |
| ↑ | 3 | 5861737 | 1999-01-19 | Goerke , et al. | — | 323 | 282 |
| ↑ | 4 | 5528132 | 1996-06-18 | Doluca | — | 323 | 284 |
| ↑ | 5 | 4962349 | 1990-10-09 | Albach , et al. | — | 323 | 265 |
| ↓ | 6 | 5015921 | 1991-05-14 | Carlson , et al. | — | 315 | 208 |

Signature

| Examiner Name | Date |
|---------------|---------|
| Sterrett | 1/12/06 |